



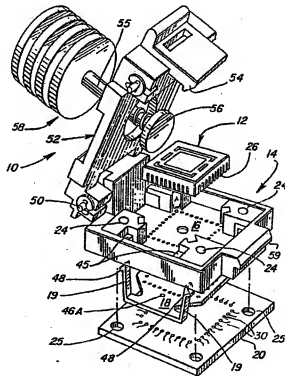
## INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

<p>(51) International Patent Classification: <b>H05K 1/14, H01R 9/09, 13/642, H01R 13/33, 11/09, 13/622, H01R 4/58</b></p>	<p>(11) International Publication Number: <b>WO 90/15517</b></p> <p>(43) International Publication Date: <b>13 December 1990 (13.12.90)</b></p>
<p>(21) International Application Number: <b>PCT/US90/02875</b></p> <p>(22) International Filing Date: <b>14 May 1990 (14.05.90)</b></p> <p>(30) Priority data: 364,343                      9 June 1989 (09.06.89)                      <b>US</b></p> <p>(71) Applicant: <b>LABINAL COMPONENTS &amp; SYSTEMS, INC. (US/US); 1521 Morse Avenue, Elk Grove Village, IL 60007 (US).</b></p> <p>(72) Inventors: <b>SHAH, Arun, Jayantilal; 2083 Lecward Lane, Hanover Park, IL 60103 (US). McClung, David, William; 9025 South Harlem Avenue, Apt. 2B, Bridgeview, IL 60455 (US). HOPFER, Albert, N.; 832 North Seeley, Park Ridge, IL 60068 (US). LINDEMAN, Richard, J.; 120 South Spruce Street, Unit 303, Wood Dale, IL 60191 (US). ZAFAR, Saeed; 2831 West Dempster, Park Ridge, IL 60068 (US).</b></p>	<p>(74) Agents: <b>CALABRESE, Joseph, P.; Neuman, Williams, Anderson &amp; Olson, 77 West Washington Street, Chicago, IL 60602 (US) et al.</b></p> <p>(81) Designated States: <b>AT (European patent), BE (European patent), CH (European patent), DE (European patent)*, DK (European patent), ES (European patent), FR (European patent), GB (European patent), IT (European patent), JP, LU (European patent), NL (European patent), SE (European patent).</b></p> <p><b>Published</b> <i>With international search report.</i> <i>Before the expiration of the time limit for amending the claims and to be republished in the event of the receipt of amendments.</i></p>

(54) Title: IMPROVED ELECTRICAL CONNECTORS AND IC CHIP TESTER EMBODYING SAME

## (57) Abstract

A novel wadded wire-plunger contact (32) is provided which possesses excellent durability and desired functional characteristics in a large variety of applications such as the testing of IC chips (12).



# DESIGNATIONS OF "DE"

Until further notice, any designation of "DE" in any international application whose international filing date is prior to October 3, 1990, shall have effect in the territory of the Federal Republic of Germany with the exception of the territory of the former German Democratic Republic.

## FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AT Austria	ES Spain	MC Monaco
AU Australia	FI Finland	MG Madagascar
BB Barbados	FR France	ML Mali
BE Belgium	GA Gabon	MR Mauritania
BF Burkina Faso	GB United Kingdom	MW Malawi
BG Bulgaria	GR Greece	NL Netherlands
BJ Benin	HU Hungary	NO Norway
BR Brazil	IT Italy	RO Romania
CA Canada	JP Japan	SD Sudan
CF Central African Republic	KP Democratic People's Republic of Korea	SE Sweden
CG Congo	KR Republic of Korea	SN Senegal
CH Switzerland	LI Liechtenstein	SU Soviet Union
CM Cameroon	LK Sri Lanka	TD Chad
DE Germany, Federal Republic of	LU Luxembourg	TG Togo
DK Denmark		US United States of America

-1-

IMPROVED ELECTRICAL CONNECTORS  
AND IC CHIP TESTER EMBODYING SAME

Field Of The Invention

This invention relates to improved electrical  
5 connectors for electrical components, and more particularly pertains to an improved device for testing integrated-circuit (IC) chips.

Background Of The Invention

IC chips are well known in the art and are in a  
10 continual state of development. Such chips may contain thousands of microscopic circuit elements and transistors, resistors, etc. Such elements are interconnected within the IC chip in various circuit configurations, and have input/output circuit leads. The chips typically are  
15 mounted in chip carriers with these input/output leads connected to conductive areas or pads which may be located about the chip carrier outer periphery or in various patterns across the bottom surface such as on land grid arrays.

20 The various chip circuits are desirably tested prior to installation in an electronic device to insure proper functioning thereof. Replacement of a faulty chip following installation in a device is both time-consuming and expensive. Accordingly, defective chips must be  
25 discovered and discarded if at all possible, prior to

-2-

installation. Testing is thus seen to be a mandatory preliminary step, particularly as the increasing complexity of IC chips results in a high-percentage failure rate. This testing normally includes circuit testing after the chip is mounted in the chip carrier by contacting the pads on the carrier with suitable test apparatus. Thus the testing and apparatus referred to hereinafter is discussed herein in terms of effecting testing contacts with the chip carrier.

- 10 The prior art has employed chip testers using metal on elastomers for conducting signals from a printed circuit trace to an IC chip carrier pad of the chip to be tested. Such elastomers however have a very limited working life and are readily damaged or deteriorate following several test cycles, requiring replacement. Obviously the testing process becomes extremely expensive when thousands of chips are tested daily on a regular basis.

- 20 Examples of prior art utilization of such conductive elastomers for use in connectors comprise Buchoff et al. U.S. Pat. 3,971,610 of July 27, 1976 and Fahling U.S. Pat. 4,360,858 of November 23, 1982.

- In accordance with this invention at least one connector comprising a wadded conductive wire button in combination with a conductive plunger is mounted in an apertured insulator board for establishing electrical contact between the pads of a chip and the circuit traces of a PC test board.

- 25 The wadded wire adapted to engage the trace of a PC board is resiliently deformable when compressed between the trace and the base of the plunger. The plunger distal and projects from the insulator opening on which disposed

-3-

for purposes of effecting substantially a point contact with a chip carrier pad by the resilient force of the button.

The use of such wadded wire conductor buttons per se in mounting boards for effecting electrical connections is shown in the prior patent art as in Smolley U.S. Pats. 4,574,331 of March 4, 1986 and 4,581,679 of April 8, 1986. Also, the use of such wadded wire buttons in testing apparatus is disclosed in Smolley U.S. Pat. 4,733,172 of March 22, 1988 in which opposed ends of the disclosed buttons are seen to engage a chip pad and a contact area of a test circuit board.

It has been found however that in the course of relative movement between an end portion of a wadded contact button protruding from a carrier board and a trace of a PC board in the course of effecting repeated desired button-pad location and contact, the wire of the button tends to unravel and cause shorting with an adjacent circuit. Such unraveling also results in non-uniform button-conductor contacts and resultant conductivity inconsistency. Thus it has been found that engagement with loose wire ends of a button contact with an engaging conductive surface will result in undesired increased resistance to current passage.

It is an object of this invention therefore to provide a chip tester employing a wadded wire contact able to effect in excess of 25,000 testing cycles without the need for contact replacement. This is made possible by utilizing in conjunction with such button contacts a plunger having an enlarged base. The plunger engages an associated button disposed within a carrier board at its inner end, and engages a pad of a chip carrier by means

-4-

of the rounded end of a needle-like projection extending exteriorly of a carrier board in which the plunger is mounted. The latter carrier-plunger contact effects negligible wear on the plunger and the resiliency of the wire button contact assures desired long extended life of the button-plunger contact combination. The button-engaging plunger may assume other configurations for effecting a low resistance contact as for instance a rounded dome-like projecting portion.

10 In the various button plunger arrangements care must be taken to design the button-receiving aperture of the button board or carrier so as to allow the button to freely compress and expand in the normal course of urging the plunger contact portion into engagement with a  
15 conductive pad or the like. Impairment of the spring action of the wadded wire buttons would adversely affect the positioning of the button ends causing variances in the compressive engagement of the button with resultant unpredictability of the resistance through the resulting  
20 button interface.

It is another object of this invention to provide a chip tester of low inductance particularly adapted for high frequency applications, having been successfully tested at 1.5 gigahertz. This is made  
25 possible by the low resistance and low inductive electrical path provided by each of the tester contacts hereinafter disclosed.

It is a further object of this invention to provide a button-plunger contact assembly which is  
30 particularly suited for rapid connect-disconnect operation and where high signal propagation is desired.

-5-

Thus in accordance with one embodiment of this invention a chip tester is provided comprising superimposed apertured insulator boards. In a first board projecting plungers are mounted for effecting substantially point contact with the pads of carriers of IC chips to be tested. In a second insulator board, wadded wire contact buttons are mounted in the apertures thereof and the opposed ends thereof project from opposed surfaces of the board in which the buttons are mounted. The buttons may thus project into the apertures of the overlying plunger board and resiliently bias the terminal plunger contact ends exteriorly of the first board.

The plunger ends may effect substantially point contacts with the pads of a chip carrier to be tested, and the button end portion projecting from the bottom of the second insulator board may effect desired contact with the trace of a test PC board.

With appropriate assembly techniques or sequences, the provided plunger-button contact portions may be disposed in the apertures of a single board and work to equal advantage.

In a modified plunger-button construction particularly adapted for quick make-and-break of electrical contacts, a wadded wire button contact is disposed in an aperture of an insulator housing and engages contact plungers at opposed button ends. The plungers have pin-like pad-engaging terminal contacts which are resiliently biased by the button contacts to project from the housing apertures in which disposed. The opposed plunger contacts may thus readily effect contacts with the contact pads or traces of PC boards or chips to

be interconnected when said pads or traces are urged against the resiliently biased plungers, as will hereinafter be described in greater detail.

In the various button and button board embodiments hereafter described, the buttons are desirably retained in button board openings in such manner as to ensure the necessary button resiliency utilized in the normal course of button operation.

The above invention will become more apparent from the following detailed description when read in light of the accompanying drawings wherein:

Fig. 1 is an exploded view of an IC tester made in accordance with this invention;

Fig. 2 is a fragmentary sectional view, partly in elevation of the chip tester of Fig. 1 in the normal course of operation and illustrated on a scale enlarged over that of Fig. 1;

Fig. 3 is an enlarged sectional view illustrating the test path present in the chip tester of Figs. 1 and 2 in the normal course of operation thereof;

Fig. 3A is an enlarged sectional view similar to Fig. 3 in which the contact button and plunger are mounted in a unitary mounting board, which may be utilized in some circumstances;

Fig. 4 is a plan view of the chip socket employed in the chip tester of Figs. 1 and 2;

Fig. 4A is a perspective view of a chip-centering clip employed in the corners of the socket of Fig. 4;

Fig. 4B is a sectional view illustrating the manner in which the clip of Fig. 4A is secured in place in the socket of Fig. 4;



-7-

Fig. 5 is a plan view of a button carrier board employed in the chip tester of Figs. 1 and 2;

Fig. 6 is an enlarged sectional view, partly in elevation, illustrating a modified contact and board arrangement employed in effecting contact between spaced printed circuit boards by a double plunger means;

Fig. 6A is a view similar to Fig. 6 in which a double-plunger and button contact arrangement as in Fig. 6 is in the process of being assembled;

Fig. 7 is a fragmentary plan view of the connection between a flex circuit and a probe card employed in the testing of integrated circuit devices;

Fig. 7A is a fragmentary sectional view illustrating a button plunger contact made in accordance with this invention particularly adapted for use in automatic testing of integrated circuits and taken on line 7A-7A of Fig. 7.

Fig. 8 is a plan view of a modified land grid array socket employing plunger button contacts made in accordance with this invention;

Fig. 9 is a sectional view taken on line 9-9 of Fig. 8;

Fig. 10 is a perspective view of the land grid array socket of Figs. 8 and 9; and

Fig. 11 is a sectional view of a modified button plunger construction illustrating a modified plunger in combination with an elongate wadded wire button.

#### Description Of The Invention

Referring now more particularly to Fig. 1 of the drawing, an exploded view is illustrated of a tester adapted to receive a chip carrier 12 during a chip test

-8-

cycle. The chip itself is not shown, being concealed within the carrier as noted above. The chip carrier 12 is receivable in socket 14 of the tester 10 on the upper surface of a plunger board 16. A button carrier board 18 having four attaching legs 19 is adapted to effect a snap latching engagement in four corner portions of the socket 14 and simultaneously be resiliently biased against the under surface of the plungers contained in board 16 in the normal course of assembly illustrated in Fig. 3. The button carrier board 18 has a printed circuit test board (PCB) 20 urged against its lower surface by means of assembly cap screw bolts 22 shown in Fig. 2. The bolts 22 traverse four corner openings 24 of the socket 14 seen in Fig. 1, through alignment bosses 15 (Fig. 2), and protrude through openings 25 of the circuit board 20 and are engaged by nuts 22N which abut the underside of the board 20, rendering the test board 20, button carrier board 18 and socket 14 in a rigid compressive state of assembly. The bosses or pins 15 and openings 25 serve as alignment means, to insure proper registry of the respective contact elements 30 and buttons 32.

As above noted, an IC chip may contain thousands of microscopic electrical circuit elements. The output/input leads for the various circuits are connected to conductive pads normally disposed about the periphery of the carrier 12. Thus, in Figs. 1 and 3, it will be noted that the carrier 12 has a plurality of contact pads 26 which are disposed about the periphery of its bottom surface. Three of such pads being illustrated on an enlarged scale in Fig. 3. As was also above pointed out, in view of the complexity of chips employed today, a high percentage of chips manufactured possess some defect

-9-

which, if uncovered following installation in an electronic device, would lead to costly and time consuming replacement and repair of such device. Accordingly, it is of great importance that the chips be tested as

5 satisfactory when used in conjunction with circuitry of a test board such as test board 20 illustrated in Figs. 1-3 so as to function satisfactorily for all purposes in the circuitry of a device in which the chip is to be installed.

10 An invention of this application may be readily appreciated from an examination of the sectional view of Fig. 3. The latter illustrates test board 20 having a trace 30 representing circuitry for testing the chip in carrier 12, e.g., representing circuitry in which the chip  
15 is normally to be employed. In accordance with this invention, electrical communication is established between the test board circuitry 30 and the pads 26 of carrier 12 by means of interconnecting compressible wire buttons 32 mounted in button board 18 and electrically conductive  
20 plungers 34 reciprocally mounted in plunger board 16.

In chip testers of the prior art, conductive elastomers are mounted in insulating boards for purposes of effecting electrical communication between a trace or pad on a test printed circuit board and a chip pad. Also,  
25 as above noted, use of conductive buttons 32 for purposes of effecting electrical connections is known in the art. Such buttons are preferably formed from a single strand of metal wire with each strand being wadded together to form a nearly cylindrical button. Each wadded wire connector  
30 32 is formed so as to fit within its receiving opening in the button board 18 as illustrated in Figs. 2 and 3 so as not to be frictionally gripped thereby so as to restrict

-10-

its ability to function as a resilient spring member. It will be noted from Figs. 2 and 3 that in those board button combinations in which the buttons 32 project from a board surface, the button-receiving opening on such surface preferably is chamfered or enlarged. Such enlargement not only facilitates entry of a button if inserted from such surface but provides space assuring retention of the buttons within the limits of the board opening despite any button "mushrooming," off-center shifting of the button ends during compression or the presence of loosened strands at the button ends. During button compression and operation of the contacts, the traces or pads preferably abut against the board surfaces as illustrated in the drawings. Also as illustrated in Fig. 3 a carrier board 18 may have a central button-engaging restriction portion R to retain the button along a central restricted annulus while allowing opposed elongate end portions of the buttons to freely compress and expand. Any button board opening design may be employed which allows desired resiliency of the buttons without gripping of the buttons so as to impair this necessary compression and expansion function. As a result, uniform button resilience and contacting is effected with resulting desired uniformity of conductivity.

The openings in the button engaging boards such as boards 18 (and 16) of Fig. 3 may be formed by injection molding of appropriate insulating materials in dies of suitable configurations to provide the openings as the boards are molded. An example of suitable materials is polyphenylene sulfide such as sold by Phillips Petroleum

-11-

Company under the tradename Ryton. Molding assures smooth aperture peripheries avoiding undesired button snagging or hangup. Fillers may be employed for strength enhancement.

Button receiving openings may also be drilled in appropriate dielectric boards, and the chamfers may be formed by appropriate drilling or reaming of the aperture ends.

Each button 32 is so sized as to protrude slightly above and slightly below the carrier board 18 in which disposed. Each button 32 effects electrical contact at multiple points with pad 30 of test board 20 at its cylindrical base and, at its opposed button end, effects similar electrical contact with the base of the respective plunger contact 34. Wadded wire button contacts such as contacts 32 provide significant operational advantages not present in other types of connections such as soldered connections. The connections provided thereby are of high integrity and reliability and have very few associated variables which may affect the quality of the electrical connection of which comprising a part. The only significant variables in contacts formed by buttons 32 are the size of the button itself and the compressive force which may be applied thereto in the course of effecting the interconnection between the opposed conductors of the connection in which the button comprises an intermediate element.

The buttons 32 are preferably formed from wire fabricated from copper alloys such as beryllium and copper, silver and copper, or copper-nickel-tin. It is apparent that when such buttons are placed under compression, they effect multiple contacts with the opposed contact areas providing multiple conductive paths. The

-12-

wadded wire buttons are so formed they are substantially resilient so that when subjected to a compressive force, each button has a tendency to return to substantially its original shape. The wadded wire connectors 34 may be of the type marketed by the Cinch Division of Labinal Components and Systems, Inc., of Elk Grove Village, Illinois, under the trademark CIN::APSE. Similar suitable buttons are available from other commercial sources.

In the normal course of use of the tester 10, the chip carrier is mounted in the recessed socket 14, the bottom of which comprises of plunger board 16 in which metallic plungers 34 are mounted for reciprocal movement. Each plunger may be formed of a durable electrically conductive metal, which is preferably coated with 200 micro inches of nickel and 100 micro inches of gold to enhance its durability. It is apparent in Fig. 3 that each plunger 34 has an enlarged cylindrical base portion 34b as well as an attached cylindrical bullet-shaped terminal portion 34t of lesser diameter having a rounded end limit to effect substantially a point contact with the engaged pad 26.

It will be noted from Fig. 4 that each corner portion of the socket 14 has mounted therein a clip 38 seen in perspective in Fig. 4A. Each clip 38 has a mounting leg 40 which is received in a recess 42 in the socket whereby it is retained and locked in position therewith either by means of a tang 43 (see Fig. 4B) or by an appropriate adhesive. Tang 43 snaps into interlocking engagement with a mating ledge portion of the socket recess in which it is disposed. A spring finger 39, resiliently joined to the mounting leg 40 by an intervening bight portion, extends angularly inward and

-13-

downward at the respective corner of the carrier-receiving cavity of the socket 14. These fingers are positioned to resiliently engage the carrier at its corners and center it in proper alignment in the socket cavity. A pair of  
5 tabs or wings 41 formed outward on the engagement leg 39 preferably define a right angular receiver to engage the respective corner of the carrier 12. Three of the recesses 42 for reception of the spring clips 38 are seen in the top plan view of Fig. 4 prior to insertion of  
10 mounting and centering springs 38; a clip 38 being shown in assembled position in the fourth corner. The outer end of leg 39 is engaged in a slot 44 in the socket for alignment and retention purposes (see Fig. 4B).

The button board or carrier 18 is snap latched  
15 into engagement against the under surface of socket 14 by means of its four flexible latching arms 19 having terminal latch portions 48. Referring further to Fig. 4, as well as Figs. 1 and 2, the socket 14 includes four apertures 46 for receiving the attaching legs 19 of the  
20 button board 18 and shoulders 45 for latching engagement by the detent shoulders 48 at the outer ends of those legs. The legs 19 are slightly divergent (see Fig. 1) and resilient to effect snap latching engagement on the shoulders 45. Referring to Figs. 1, 2 and 5, alignment  
25 means are provided by way of an aperture 46A and a slot 46B in board 18 and mating depending pins 47 on the socket to effect proper registry of the button board 18 with the plunger board section 16. These components provide attachment and alignment of the button board, and  
30 attendant retention of the plungers 34 in the plunger board section 16, prior to attachment of the testing assembly 10 to the test circuit board 20. The latching

-14-

arms 19 may be replaced by other securement means to maintain the elements of the tester unit in a desired state of assembly. For example, button board 18 may also be received in a press fit in a mating recess in the bottom of the plunger board 16.

Prior to insertion of chip carrier 12, the plunger board section 16 of socket 14 is already in desired compressive engagement with underlying buttons 32 and button board 18 as well as the test printed circuit board 20 in the assembled relationship illustrated in Fig. 2. As earlier noted, the entire assembly is maintained in a compressive state following threaded engagement of the nuts 22N on the assembly bolts 22, urging the test circuit board 20 upwardly against the bottom of the button board or carrier 18 seen in Fig. 5.

During chip testing the circuit test board 20 and button board 18 are in assembled relationship with the plunger housing 16, with the chip carrier 12 urged into desired centered relationship in the socket 14 by corner springs 38. In such centered position the chip carrier is in supported engagement on top of the plungers 34. Cover 52 is then pivoted downwardly from the position of Fig. 1 of the drawing into the position overlying carrier 12. A spring-loaded plunger 55 has an enlarged base portion 56 comprising a component of a heat sink 58 and biases the chip carrier 12 into desired engagement with the underlying projecting terminal portions 34t of the plungers 34 mounted in housing portion 16 comprising the floor of socket 14. Prior to downward movement of the hinged cover 52 which pivotally moves about pivot axis 50, the upper plunger terminal portions 34t are in the dotted line position illustrated in Fig. 3 as a result of the



-15-

uncompressed conditions of the wadded wire contacts 34. The uncompressed buttons 32 thus upwardly bias plungers 34 toward a limit portion where the upper surface portions of the base portions 34b strike the upper counterbore surfaces of the plunger recesses in which the plungers 34 are disposed. Downward pivotal movement of the tester cover 52 forces the bottom of the carrier contact pads 26 to downwardly move the plunger contacts 34, simultaneously compressing the underlying button contacts 32 into the condition illustrated in Fig. 3. The latter compressed condition assures a desired low resistance, low inductance, high speed electrical connection between the bottom of each chip pad 26 and its associated trace 30 of the test printed circuit board 20. The cover 42 may have means such as latch 54 which effects a snap-lock engagement with mating latch portion 59 of the socket 14 as clearly illustrated in Fig. 2.

After a chip 12 has been tested, the tester cover 52 may be readily unlatched from the socket 14 and pivotally moved upwardly and into the position of Fig. 1 for removal of the carrier with the tested chip and insertion of a new chip carrier. As the test circuit board 20, button board 18 and socket 14 need not be disturbed for the testing of subsequent chips, a succession of carriers may be readily inserted for testing of the carried chips without disturbing any portion of the assembly of Fig. 3 below the lower surface of the carrier 12.

The bottom of the socket 14 of the tester 10 may have housed therein not only the plungers 34 but also the buttons 32. Fig. 3A illustrates a unitary board 16M housing both button contacts 32 and plungers 34. This

-16-

would require that the board 20 be attached, or other means provided, for retention of the buttons and plungers in oversize apertures in board 16M. Similarly, the bottom of the socket 14 may be a thin apertured sheet against which a discrete plunger board and a discrete carrier board and test circuit board may be urged and retained in a condition of assembly by means such as the illustrated assembly bolts 22 or other securing means.

It has been found that if buttons 32 are employed for purposes of effecting direct communication between test circuit board pads 30 and carrier pads 26, the horizontal movement of chip carriers during positioning to the desired location in socket 14 will tend to gradually unravel the upper surface of buttons 32. Such wire unraveling could lead to possible shorting between adjacent pads 26, which in many instances are spaced only a few mils apart, and interfere with the establishing of a desired uniform contact between the pads 26 and the tops of the contact buttons.

In accordance with this invention, by utilization of a two-piece intermediate connector comprising the button 32 and plunger contact 34 between the traces of the test circuit board and the chip contacts, desired uniform electrical communication is established between the board 20 and the chip carrier 12. The only contact effected between each plunger and an engaged pad 26 is in the nature of a point contact under compression. The carrier in the normal course of being located in the recess of the socket 14 of the tester 10 may readily slidably move relative to an engaged plunger terminal contact portion 34t without any damage whatsoever to its engaged, associated button contact 32.

-17-

It will be appreciated, for instance, that during the pivotal downward movement of the hinged cover 52 of the tester 10 of Fig. 1, engagement between the base 56 of the spring loaded heat sink 58 and the upper surface of the carrier 12 would normally effect a force component of movement tending to slidably move the carrier 12 away from the pivot axis 50. Such force component has no adverse effect on each contact made between the terminal portion 34 of each plunger contact 34 and each pad 26. However, if the contact were effected directly between a pad 26 and the upper surface of a wadded wire contact 32, such relative slidable movement would have a tendency to unravel the wadded wire upper surface of each contact button 32, leading to the undesired shorting problem and inefficient engagement previously noted.

The arrangement of Figs. 8 through 10 comprises a working carrier mount and connector assembly in which a grid array in the button board provides for various conductive paths between a variety of board traces and overlying chip pads. Fig. 8 illustrates a chip mounting device in which a chip carrier such as carrier 12 (fragmentarily illustrated in Fig. 10) is adapted to be centered in a socket body 62 having a plurality of centering clips 64 disposed about the periphery of the socket body. Plunger openings 65 are formed in an array in bottom of socket 62 so as to accommodate various pad arrangements of land grid array carriers adapted to be mounted in socket 62. After the carrier is centered in the socket body by means of inverted U-shaped springs 64, an overlying retention spring 63 which is interlocked to peripheral portions of the socket 62 as illustrated in Fig. 10 urges the chip carrier 12 downwardly with a

-18-

desired, predetermined biasing force against underlying plunger contacts 34 and subjacent wadded wire buttons 32. The distal terminal portions 34t of the plungers 34 are depressed and the buttons 32 compressed following chip  
5 insertion into the socket.

The plunger contacts 34 in Figs. 8 and 9 are arranged in grid array wherein the desired plunger contacts and their associated button contacts 32 may be employed for purposes of effecting desired interconnection  
10 between traces of a circuit board 68 and the bottoms of the buttons 32 as illustrated in Fig. 9. It will be noted from Fig. 9 that the buttons 32 may be detachably associated with socket 62 for ready button replacement if and when necessary by a replaceable housing board 67 which  
15 may interlock in a bottom recess of the socket 62 by means of a press fit or equivalent means of detachable securement.

The socket button board assembly of Fig. 9 may be readily mounted to a printed circuit board by means of  
20 alignment and mounting posts 70. The posts may be received in a friction interference fit in corresponding mounting openings of such circuit boards such as circuit board 68.

The sectional view of Fig. 7A comprises a  
25 section taken on line 7A-7A through an opening of an assembly including probe card 79 illustrated in plan view in Fig. 7. Figure 7A illustrates an assembly wherein wadded wire button contacts 32 and associated plunger contacts 34 of this invention may be employed for purposes  
30 of effecting contact in electrical communication with plated through openings such as illustrated openings 80 of probe card 79, the latter being mounted over a plunger

-19-

board 84 having compressible contact buttons 32 engaging the bottom thereof. The resiliency of buttons 32 mounted in board 81 urges engaged plunger contacts 34 against the end of the plated through openings 80 for purposes of effecting electrical communication between the probe card circuitry and circuitry 86 of a flexible substrate 71. The outer end 34t of the button is generally dome-shaped and of a diameter greater than the diameter of the plated through opening and insures good electrical contact with the hole plating. The bottoms of the button contacts 32 are in engagement with the flex circuit 86 as illustrated. For purposes of maintaining the probe card and flex circuit in desired alignment, aligning pins (not illustrated) traversing openings such as openings 87 of Fig. 7 may be employed. The entire assembly may be mounted above a rigid backing plate such as plate 88 of Fig. 7 on which an insulating layer 90 is disposed to insulate the rigidifying backing plate 88 from the conductor contacts 32 mounted in the button board.

Another application of the novel wadded wire buttons and plunger contacts of this invention is seen in Figs. 6 and 6A of the drawing wherein it will be noted that opposed plunger boards 94 and 96 may have spaced plungers 34 mounted therein at opposed ends of compressible wadded wire contacts 32. Fig. 6A is an exploded view of this connector arrangement, as in the course of assembly. This double plunger contact assembly may be used to advantage for ready connect-disconnect operations between engaged circuits or traces of spaced printed circuit boards such as printed circuit boards 100 having traces 102. The size of the wadded wire buttons 32 is such relative to the receiving openings in the opposed

-20-

boards 94 and 96 that desired resilient engagement is made between the plunger contacts 34 as they are urged into compressive engagement with the spaced boards 96 and 94, simultaneously compressing the wadded wire buttons therebetween. Terminal contact portions 34t of the plunger contacts 34 may be urged into compressive engagement with the traces 102, thus effecting good electrical contact between the spaced printed circuit boards. Contact may be readily broken by removing either printed circuit board.

A knurled pin 91 is force-fit in holes 92 of the two boards for alignment and retention purposes. The two discrete boards 94 and 96 also may be joined as by sonic welding or adhesives along juncture line 97 to effectively form a single "board" 95 for permanently capturing the buttons and plungers as in Fig. 6. When the buttons 32 are captured within the board 95 no chamfering of the button openings is necessary to compensate for "mushrooming" exteriorly of the boards. The buttons must be so sized relative to the openings in which the buttons and plungers are disposed so as to assure free button compression and expansion. The board apertures in which the buttons are disposed may of course be of any desired configuration to accommodate desired button expansion.

Figure 11 demonstrates another variety of plunger configuration will work to advantage in the novel connectors of this invention. Figure 11 illustrates a button 32 mounted in an opening 110 extending through a button carrier board 112, with the lower button end exposed for engagement with an appropriate contact component such as a PC board pad or trace. The upper button end portion extends through a large counterbore

-21-

opening 114 which also accommodates a dome-shaped contact element 116 arranged over the upper end of the button 32. The dome-shaped contact 116 is reciprocally movable within the opening 114. A retainer sheet 118, with an opening 5 120 therein which is smaller than the outer diameter of the flange portion of the contact 116, is bonded to the top surface of the board 112 and overlaps the flange 110 of the contact 116 to capture the contact and retain it in position on the wadded button 32 for its reciprocal 10 movement. Enlarged portions of the opening 110, including chamfered portion 122 and the counterbore section 114, provide freedom of compressive movement of the respective end portions of the button 32 and the other attendant advantages noted above. Plungers 116 may be readily 15 stamped from a mother sheet and readily assembled in large numbers in the button carrier board 112.

It is thus seen that novel plunger-wadded wire button connector assemblies have been provided in which the full benefits of the wadded wire contacts are obtained 20 in applications such as chip testing and other repetitive circuitry contact situations without destructive consequences to the wadded wire contacts. The provided assemblies isolate an end of a wadded wire button contact from forces tending to disintegrate or unravel such wadded 25 wire button end. As above noted, such unraveling would have a tendency to result in shorting in the course of contacting applications wherein the intervals between the conducting trace or pad surfaces are extremely small. The wadded wire button contacts of the disclosed connectors 30 and testers are extremely durable in compression and are relatively inexpensive to manufacture. Because of the short intervals between the connected circuits of the chip

-22-

carrier and circuit board, signal distortion occasioned by inductance is substantially eliminated. The point contacts affected by the plunger contacts provide ready relative movement between an engaged pad or trace without  
5 any resulting structural damage. As the contacts effected by the plunger contacts are in the nature of point contacts the alignment tolerances of the chip pads or circuit traces or pads relative to the plunger contact is increased.

10 As above noted, many structural changes and alternative arrangements may be employed without departing from the ambit of the invention disclosed. Accordingly, it is intended that this invention be limited only by the scope of the appended claims.

15 What is claimed is:



-23-

1. An electrical connector comprising a dielectric housing means having contacts mounted therein and having opposed first and second surfaces for assembly with opposed conductors for electrical interconnection; said housing having at least one prearranged aperture traversing the thickness thereof and extending between aperture openings disposed in said opposed first and second surfaces; a plurality of conductive contact elements disposed in each of said apertures comprising a first resilient contact element partially disposed within said aperture and having a terminal end with a surface area substantially equal to the cross-section of the opening from which projecting on the first housing means surface, and a second contact element in electrical communication with said first contact element and having a terminal end portion projecting from the second housing means surface and adapted to effect a point-like contact with a conductor surface disposed at substantially right angle to said terminal end.

2. An electrical connector comprising a dielectric housing means having contacts mounted therein and having opposed first and second surfaces for assembly with opposed conductors for electrical interconnection between said conductors; said housing means having at least one prearranged aperture traversing the thickness thereof and extending between said opposed first and second surfaces; a first conductive contact element disposed in said aperture comprising a wadded strand of conductive wire which is resiliently deformable when compressed, and projecting from the first surface of said

-24-

dielectric housing means; a second conductive contact element disposed in said housing means aperture and having a first terminal portion disposed in said aperture in engagement with said wadded wire strand; said second contact element having a terminal portion projecting from the second housing means surface.

3. The electrical connector of claim 1 or 2 in which said second contact element comprises a plunger having an inner base portion engaging said wadded wire strand and connected to said projecting terminal portion; said housing means aperture being configured for mating engagement with said plunger whereby reciprocal movement of said plunger in said aperture is axially guided.

4. The electrical connector of claim 1 or 2 in combination with a socket for mounting an IC chip carrier for circuit testing and having a plurality of peripherally mounted contact pads; said dielectric housing means being disposed on the bottom of said socket whereby portions of second contact elements mounted in said connector are urged into engagement with contacts of an IC chip carrier when mounted in said socket; and means for locating a test circuit board having prearranged traces for engagement with terminal end portions of said first resilient contact elements whereby electrical communication is established between said traces and said chip carrier pads.

5. The electrical connector of claim 1 or 2 in combination with a probe card mounted over said dielectric housing means first surface and having a

-25-

plurality of prearranged plated-through openings at which a plurality of prearranged second contact elements of said electrical connector are received in electrical engagement, and a flex circuit disposed adjacent said dielectric housing means second surface and having a plurality of prearranged pads in engagement with terminal ends of certain of said first contact elements.

6. The electrical connector of claim 1 or 2 in which said dielectric housing means comprises two superimposed dielectric boards having correspondingly arranged apertures therein; said first contact elements being mounted in apertures of one of said boards; said connector second contact elements being mounted in the apertures of the other of said boards and being resiliently biased by said first contact elements.

7. An electrical connector comprising a dielectric housing having opposed first and second surfaces; at least one prearranged aperture traversing the thickness of said housing and extending between said first and second surfaces; a plurality of conductive contact elements disposed in each of said apertures; said contact elements comprising a wadded strand of conductive wire which is resiliently deformable when compressed; second and third conductive contact elements engaging said wad by means of inner ends thereof and having distal terminal ends projecting from opposed surfaces of said housing; said second and third contact elements being normally outwardly biased by said wad whereby the terminal ends are disposed exteriorly of said aperture; said wad strand being sufficiently compressible whereby the second

-26-

and third conductive contact element terminal ends may be urged into substantially coplanar relation with said housing opposed surfaces.

8. An electrical connector for spaced PC boards comprising a pair of dielectric boards having a plurality of corresponding apertures extending there-through whereby at least some of said apertures are in alignment when said boards are superimposed on each other; plunger-like contacts disposed in at least some of the aligned apertures of said boards and having enlarged base portions and rounded terminal ends adapted to effect point-like contacts upon projecting from outer surfaces of each of said boards; a wadded strand of conductive wire, which wad is resiliently deformable when compressed, disposed in at least some of said aligned apertures and engaging opposed plunger base portions when said dielectric boards are urged into adjacent relationship; said wadded strands being so formed relative to the board apertures in which disposed that the terminal ends of the plungers engaged by said strands are resiliently urged to project from opposed outer surfaces of said dielectric boards when adjacent surfaces of said boards are moved into adjacent relationship.

9. The electrical connector of claim 8 in combination with spaced PC boards having conductive traces; said traces being in engagement with terminal plunger ends of said connector; and means for maintaining said PC boards and dielectric boards in desired vertical alignment relative to each other and in a desired state of compression.

-27-

10.. The electrical connector of claim 4 in combination with an IC chip carrier mounted in said socket; carrier locating means mounted in corner portions of said socket for desired location of said carrier in said socket, and means for urging said carrier into compressive engagement with said dielectric housing means second surface.

11. The electrical connector of claim 10 in which the carrier urging means comprises a spring loaded heat sink.

12. The electrical connector of claim 1 or 2 in which said dielectric housing means comprises discrete dielectric boards; one of said boards having said first connector elements mounted therein and another of said boards mounting said second connector elements therein.

13. The electrical connector of claim 12 in which said dielectric board having said second connector elements mounted therein has latching means attached to peripheral portions thereof for snap engagement with mating portions of said socket.

14. The electrical connector of claim 1, 2, or 7 in which the apertures of the boards in which the resilient contact elements are disposed, are chamfered at the aperture openings from which such resilient contact elements project for preventing mushrooming of the resilient contact elements exteriorly of the apertures.

-28-

15. The electrical connector of claims 1, 2 or 7 in which the resilient contact elements are free of excessive frictional engagement with the aperture peripheries in which disposed and function as resilient springs.

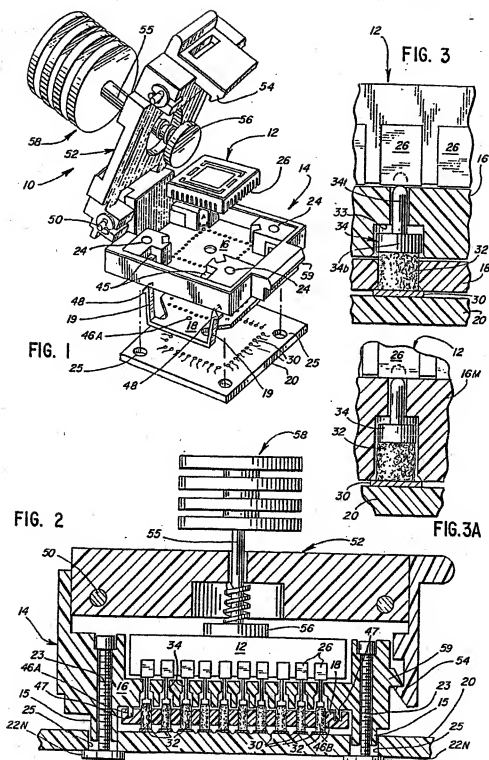


FIG. 4

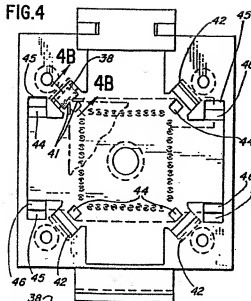


FIG. 4A



FIG. 4B



FIG. 5

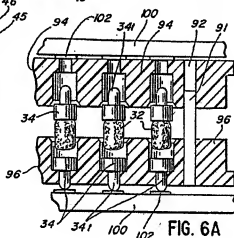
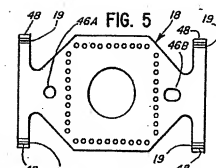


FIG. 6A

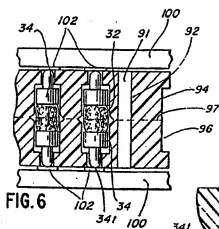


FIG. 6

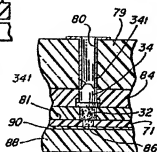


FIG. 7A

